

In Place of FORM PTO-1449 (Modified)

Serial No.:

Applicant:

Michael Eneboe, et al.

Filing Date:

October 30, 2001

Group:

Atty. Docket No.: 01-489

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**Jc858 U.S. PTO  
10/021619

Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA	6,152,613	11/28/00	Martin et al.	395	800.33	11/25/97
ABA	5,898,677	04/27/99	Deeley et al.	370	276	01/13/97
ACA	5,752,070	05/12/98	Martin et al.	395	800.33	07/08/94
ADA						
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AKA						
ALA						
AMA						
ANA						
AOA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner Initial

APA

"On-chip Networks weighed as wiring alternative," by Ron Wilson, Integrated System Design, June 25, 2001;  
<http://www.esd.com/story/OEG20010625S0099>, July 3, 2001.

AQA

"Configurable Computing," by John Villasenor and William H. Mangione-Smith; Scientific American, June 1997,  
<http://www.sciam.com/0697issue/0697villasenor.html>, August 7, 2001.

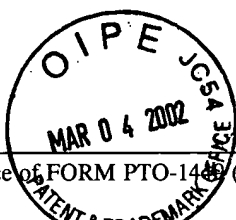
ARA

Examiner:

Date Considered:

25-July-2003

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Serial No.: 10/021,619  
Applicant: Michael Eneboe, et al.  
Filing Date: October 30, 2001  
Group:  
Atty. Docket No.: 01-489

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
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**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
___ AAA						
___ ABA						
___ ACA						
___ ADA						
___ AEA						
___ AFA						
___ AGA						
___ AHA						
___ AIA						
___ AJA						

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**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
___ AKA						
___ ALA						
___ AMA						
___ ANA						
___ AOA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner  
Initial

\_\_\_ APA "Self-Reconfigurable Programmable Logic Device", Reetinder Sidhu, et al., University of Southern California, Office of Technology Licensing, Los Angeles, CA, September 7, 2001.

\_\_\_ AQA  
\_\_\_ ARA

Examiner:

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*25-July-2003*

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PTO/SB/08A (10-96)  
Approved for use through 12/31/99. OMB 0651-0031  
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE



# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	10/021,619
Filing Date	October-30, 2001
First Named Inventor	Michael Eneboe
Group Art Unit	2123
Examiner Name	
Attorney Docket No.	/ 01-489

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Sheet 1

of

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## U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	U.S. Patent Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6,269,277		Hershenson et al.	07-31-2001	

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
		Self-Reconfigurable Programmable Logic Device - Author(s) - Sidhu et al.	

Examiner  
signature

Date  
considered

25-July-2003

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